

What is claimed is:

1. A liquid crystal display device comprising:  
a plurality of gate lines each having a repair pattern for at least one unit pixel;  
a plurality of data lines arranged to cross the gate lines;  
a pixel electrode formed at a unit pixel region defined as one of the gate line and one of the data lines intersect; and  
a storage line overlapping with the pixel electrode.
2. The device of claim 1, wherein the repair pattern is formed overlapping with the pixel electrode.
3. The device of claim 2, wherein the repair pattern includes at least one protrusion overlapping with the pixel electrode.
4. The device of claim 1, wherein the repair pattern includes protrusions extending in opposite directions from a portion of the gate line, so as to be formed under two adjacent pixel electrodes.
5. The device of claim 1, wherein the repair pattern electrically connects the corresponding gate line and pixel electrode when the corresponding gate line and pixel electrode are welded to each other via the repair pattern.
6. The device of claim 1, wherein the device has a normally white mode.
7. The device of claim 1, wherein the device has is a storage-on-common type.
8. The device of claim 1, further comprising:  
a thin film transfer for each unit pixel.
9. A liquid crystal display device comprising:  
a plurality of gate lines formed in parallel to each other;

a plurality of data lines formed in parallel to each other and crossing the gate lines;

a pixel electrode formed at a unit pixel region defined by one of the gate lines and one of the data lines, and having a repair pattern; and

a storage electrode overlapping with the pixel electrode.

10. The device of claim 9, wherein the repair pattern overlaps with the corresponding gate line for each unit pixel.

11. The device of claim 9, wherein the repair pattern includes at least two protrusions extending in opposite directions to overlap with adjacent gate lines.

12. The device of claim 9, wherein the pixel electrode is electrically connected to the corresponding gate line via the corresponding repair pattern as heat is applied to the corresponding repair pattern.

13. The device of claim 9, wherein the device has a normally white mode.

14. The device of claim 9, wherein the device is a storage-on-common type.

15. The device of claim 9, further comprising:  
a thin film transistor for each unit pixel region.

16. A redundancy repair pattern structure usable in a display device, the device including a plurality of gate lines and data lines crossing each other, a plurality of pixel electrodes each at an intersection of the corresponding gate and data lines, and a plurality of storage lines parallel to the gate lines, the structure comprising:

a redundancy repair pattern extending from each of the gate lines and formed under a pixel electrode.

17. The redundancy repair pattern structure of claim 16, wherein the repair pattern includes protrusions extending in opposite directions from a portion of the gate line, so as to be formed under two adjacent pixel electrodes.

18. The redundancy repair pattern structure of claim 16, wherein the repair pattern electrically connects the corresponding gate line and pixel electrode when the corresponding gate line and pixel electrode are welded to each other via the repair pattern.

19. A redundancy repair pattern structure usable in a display device, the device including a plurality of gate lines and data lines crossing each other, a plurality of pixel electrodes each at an intersection of the corresponding gate and data lines, and a plurality of storage lines parallel to the gate lines, the structure comprising:

a redundancy repair pattern extending from each of the pixel electrodes and overlapping with at least one gate line.

20. The device of claim 19, wherein the repair pattern includes at least two protrusions extending in opposite directions to overlap with adjacent gate lines.

21. The device of claim 19, wherein each pixel electrode is electrically and selectively connected to the corresponding gate line via the corresponding repair pattern as heat is applied to the corresponding repair pattern.

22. A method for providing a liquid crystal display device, the method comprising:

forming a gate line having a repair pattern on a substrate;  
forming a gate insulation film, a semiconductor layer and a conductive layer on the substrate;  
forming source/drain electrodes;  
forming a passivation layer on the source/drain electrodes; and  
forming a pixel electrode on the passivation layer such that the pixel electrode overlaps with the repair pattern.

23. The method of claim 22, wherein the pixel electrode and the gate line are electrically connected to each other by irradiating a beam to the repair pattern.

24. A method for providing a liquid crystal display device, the method comprising:

- forming a gate line on a substrate;
- forming a gate insulation film, a semiconductor layer and a conductive layer on the substrate;
- forming source/drain electrodes;
- forming a passivation layer on the source/drain electrodes; and
- forming a pixel electrode having a repair pattern on the passivation layer such that the repair pattern overlaps with the gate line.

25. The method of claim 24, wherein the pixel electrode and the gate are electrically connected to each other by irradiating a beam to the repair pattern.